

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: To be assigned
Filing Date: Herewith
Applicants: Yoshifumi Okabe et al.
Group Art Unit: To be assigned
Examiner: To be assigned
Title: Semiconductor Device And Method Of Manufacturing Same
Attorney Docket: 4041J-000499/DVE

Director of The United States Patent and Trademark Office
P.O. Box 1450
Alexandria, Virginia 22313-1450

PRELIMINARY AMENDMENT

Sir:

Prior to examination of the present application, please consider the following.

Amendments to the Specification being on page 2 of this paper.

Amendments to the Claims begin on page 3 of this paper.

Remarks begin on page 5 of this paper.